Indian Institute of Information Technology Design and Manufacturing, Kurnool
Department of Electronics and Communication Engineering
Scheme for **M. Tech. Electronic System Design (ESD)**

Semester I				
Sl. No.	**Course Name**	**Category**	**I-P**	**Credits**
1	Mathematical Foundations for ESD	ELE	3-0	3
2	Product Design and Development	DES	3-0	3
3	Product Design and Development Practice	DES	0-3	2
4	Digital VLSI System Design	PEC	3-0	3
5	Digital VLSI System Design Practice	PEC	0-3	2
6	Departmental Elective-I	PEC	3-0	3
7	Departmental Elective-II	ELE	3-0	3
8	Embedded System Practice	PEC	0-2	2
9	Seminar	PCD	---	2
			Total	**23**
Semester II				
Sl. No.	**Course Name**	**Category**	**I-P**	**Credits**
1	Circuits for Electronic System Design	PEC	3-0	3
2	Circuits for Electronic System Design Practice	PEC	0-3	2
3	Electronic Systems Packaging	PEC	3-0	3
4	Electronic Systems Packaging Practice	PEC	0-3	2
5	Digital Signal Processing and Architectures	PEC	3-0	3
6	Digital Signal Processing and Architectures Practice			3
7	Departmental Elective-III	ELE	3-0	3
8	Free Elective-I	ELE	3-0	3
9	Comprehensive Viva-Voce	PCD	---	2
			Total	**24**
Semester III				
Sl. No.	**Course Name**	**Category**	**I-P**	**Credits**
1	Dissertation Work-I	PCD	0-25	10
			Total	**10**
Semester IV				
Sl. No.	**Course Name**	**Category**	**I-P**	**Credits**
1	Dissertation Work-II	PCD	0-25	20
			Total	**20**
			Total Credits	**77**