# INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, DESIGN AND MANUFACTURING, KURNOOL,

Jagannathagattu, Dinnedevarapadu, Kurnool, Andhra Pradesh - 518007
(An Institute with National Importance under MHRD, Govt. of India)

No. IIITDMKL/ECE/2020/07                                      Date : 03 Feb 2020

## Notification for the recruitment of the post "Junior Research Fellow" to work on the DST-SERB sponsored research project in the department of Electronics and Communication Engineering, IIITDM Kurnool

Applications are invited for the following temporary post to work on the DST-SERB sponsored research project entitled **"Engineering of High Performance Signal Processing Elements for Real-time Infrastructure"**.

**Name of the Post:** Junior Research Fellow

**Number of posts:** One
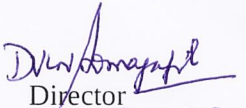
**Minimum Qualification and Experience:**

B.E/B.Tech in Electrical/Electronics Engineering with valid gate score  AND/OR

M.E/M.Tech in Microelectronics/VLSI design with valid gate score.

**Desirable Qualification and Experience:** Candidate having experience in Digital System Design, RTL design using Verilog/VHDL, IP integration using Xilinx Vivado, Custom IP development using Xilinx Vivado, and Exploitation of various legacy IPs such as Ethernet, 10GBaseR, BRAM, and so on.

**Salary:** Rs. 31,000 per month (consolidated)

**Principal Investigator of the Project:** Dr. Mohamed Asan Basiri M, IIITDM Kurnool, Email – asan@iiitk.ac.in

Director
3.2.2020

**Instructions to the Applicants:**

1) The post is purely temporary and is for the duration of one year. It may be extended for one more year depending on the requirement and the performance of the candidate.

2) Only short-listed applicants will be called for the written test followed by an interview. Applicants who do not meet the required qualification will not be called for the interview. The institute reserves the right to fix suitable criteria for short listing of eligible candidates satisfying qualification and experience. No interim correspondence / inquiries will be entertained from the candidates in connection with the process of shortlisting.

3) The candidate is responsible for the correctness of the information provided in the application. If it is found at a later date that any information given in the application is incorrect / false the candidature / appointment is liable to be cancelled / terminated. Incomplete/ partially filled application forms/ application without the supporting documents will not be considered.

4) The duly filled/ signed application along with the self attested copy of the testimonials should be sent by post to the following address.

Dr. Mohamed Asan Basiri M, Assistant Professor, Department of ECE, IIITDM, Jagannathagattu, Kurnool – 518007.

5) Envelope containing application form should be super-scribed with "Application for the post of JRF : SERB-DST Project - 2020".

6) Applications which are not strictly as per the format given will be summarily rejected.

7) Canvassing in any form will disqualify the candidature of the applicant.

8) No TA/DA will be provided to attend the interview.

9) The original testimonials will be verified at the time of written test/interview. The last date of receipt of the completed application form is 18/02/2020.

9) Applicants are requested to look at website of IIITDM Kurnool (www.iiitk.ac.in) from time to time for information, updates etc. Updations, if any will be published on the website only and not in newspaper.

Director 3.2.2020

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**Application for the recruitment of the post "Junior Research Fellow" to work on the DST-SERB sponsored research project in the department of Electronics and Communication Engineering, IIITDM Kurnool**

| 1. | Name of the Candidate | |
|----|----------------------|---|
| 2. | Date of Birth | |
| 3. | Marital Status | SINGLE/MARRIED |
| 4. | Gender | MALE/FEMALE |
| 5. | Nationality | |
| 6. | Community | GEN/OBC/SC/ST |
| 7. | PwD | YES/NO |
| 8. | Email | |
| 9. | Phone/Mobile Number | |
| 10. | Permanent Address | |
| 11. | Address for Communication | |
| 12. | Particulars of the GATE<br><br>GATE Score<br>Year of Passing<br>Department | |

13. Academic Record

| Certificate/ Degree | Specialization | Name of the Institute | Name of the Board/ University | Year of Passing | Percentage/ CGPA |
|---------------------|----------------|-----------------------|-------------------------------|-----------------|------------------|
| SSLC (10th) | | | | | |
| HSC (+2) | | | | | |

| | | | | |
|---|---|---|---|---|
| B.E/B.Tech | | | | |
| M.E/<br>M.Tech | | | | |

14. Experience (Industry/ Research/ Teaching)

| Designation | Name of the Organization | Duration | | Nature of Work |
|---|---|---|---|---|
| | | From | To | |
| | | | | |
| | | | | |
| | | | | |

15. Any Other Information

**I declare that the above given information are true to my knowledge and belief. If any discrepancy is observed, my selection may be cancelled at any stage.**

**Date:**
**Place:**                                                    **Candidate's Signature**

**Note: Attach all the supporting documents.**