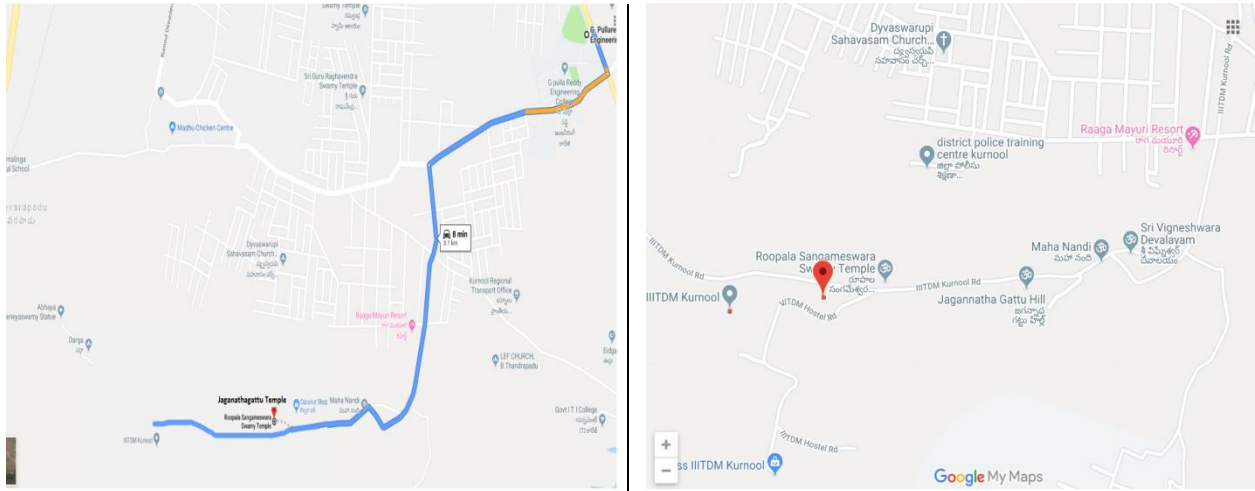


1 INTRODUCTION

Indian Institute of Information Technology, Design and Manufacturing (IIITDM), Kurnool was youngest among five centrally funded IIITs and established as part of Andhra Pradesh Reorganization act in the academic year 2015-16 at the historical city of Kurnool in Rayalaseema region and recognized as an Institution of National Importance by an act of Parliament. Presently, the institute is having a permanent campus at Jagannathagattu, Dinnedevrapadu, Kurnool, which is listed below.



Location Map of Indian Institute of Technology, Design and Manufacturing, Kurnool, Andhra Pradesh, India

The institute has initially started B. Tech. programs in Computer Engineering, Electronics and Communication Engineering and Mechanical Engineering. The current intake stands at 143 students. Our present total student strength is 406. Indian Institute of Information Technology, Design and Manufacturing, Kurnool was mentored by Indian Institute of Information Technology, Design and Manufacturing, Kancheepuram till February 2019.

Currently 11 regular faculty, 12 visiting/Guest/Contract faculty, and 13 non-faculty (supporting staff) are serving the academic and administrative activities of the institute. All our faculties are having Ph. D. degrees from reputed institutes. In addition, medical, house-keeping, security and other services were hired through external agency to create a safe and hygienic environment.



Existing Campus



Proposed Campus

The first Convocation was conducted and degrees were awarded to all eligible students in the month of August 2019. In addition to this, all entitled first batch students have secured placements in reputed industries. Campus is being developed in 190 acres of land on the hilltop located at Jagannathagattu, Kurnool city, which is adjacent to Nandyal – Kadapa highway, initiated in the year 2016 and is expected to complete in all aspects by 2022. As part of this, CPWD has already taken up the construction activity for two hostel blocks to accommodate 350 students and one mess block which is expected to be ready by August 2019. Higher Education Financing Agency (HEFA) has sanctioned an amount of INR 218 crores for ongoing and future construction activities at the campus. In this connection, an agreement with HEFA and MoU with CPWD were signed respectively on August 30, 2019 and 16th September 2019.

The institute’s goal is to provide aesthetically pleasing, environment-friendly green campus facilities to enhance the learning, teaching and inter-disciplinary research activities for all stakeholders. The objective of this institute is to carry the flagship of Indian Institute of Information Technology, Design and Manufacturing, Kurnool to greater heights by applying their knowledge in an inter-disciplinary manner to provide solutions for various industrial, and research and development projects holding responsible ethical values.

2. ADMINISTRATIVE, ACADEMIC STATUTORY BODIES AND OTHER COMMITTEES:

The different committees constituted at the institute in the year 2018-19 are:





2.1 Board of Governors (BoG)

2.2 Finance Committee (FC)



2.3 Building and Works Committee (BWC)

2.4 Senate

2.1 Board of Governors:

S. No.	Photographs	Description
1	 Chairperson	Prof. H.A. Ranganath, M.Sc., Ph.D., FASc., FNASc., FNA., FISEB Distinguished Professor (for life) of University of Mysore (Former Vice Chancellor, Bangalore University; Former Director, NAAC)
2	 Member	Shri. K. Vijayanand, IAS Bureau Head Principal Secretary (FAC) Govt. of Andhra Pradesh
3	 Member	Dr. Sukhbir Singh. Sandhu Additional Secretary (TE) Ministry of HRD, Govt. of India
4	 Member	Dr. Jaideep Kumar Mishra, Ph.D. Joint Secretary and Group Coordinator Ministry of Electronics and Information Technology, Govt. of India

5	 Member	Prof. K. N. Satyanarayana, Ph.D. Director Indian Institute of Technology Tirupathi
6	 Member	Prof. M. Chandrasekhar, Ph.D. Director Indian Institute of Management Vishakhapatnam
7	 Member	Prof. Banshidhar Majhi, Ph.D. Director, Indian Institute of Information Technology, Design and Manufacturing Kancheepuram
8	 Member	Shri. Venkata Narasimham Peri Founder & CEO Cognitive care Hyderabad
9	 Member	Prof. N V Ramana Rao, Ph.D. Director, National Institute of Technology Warangal
10	 Member	Smt. Sashi Sairaman CEO, MTAB Chennai
11	 Member	Prof. D. Janakiram Professor of Computer Science and Engineering Indian Institute of Technology Madras



12	 Member	Prof. Aparajitha Ojha, Ph.D. Professor of Computer Science & Engineering Indian Institute of Information Technology, Design and Manufacturing Jabalpur
13	 Member & Secretary	Prof D V L N Somayajulu, Ph.D. Director & Registrar I/c Indian Institute of Information Technology Design and Manufacturing Kurnool




2.2 Finance Committee:

S. No.	Photographs	Description
1	 Chairperson	Prof. H.A. Ranganath, M.Sc., Ph.D., FASc., FNASc., FNA., FISEB Distinguished Professor (for life) of University of Mysore (Former Vice Chancellor, Bangalore University; Former Director, NAAC)
2	Member	Shri. Prashant Agrawal, Director (IIITs) Dept. of Higher Education Ministry of HRD Government of India Delhi
3	Member	Shri. Anil Kumar Director (Finance) Dept. of Higher Education Ministry of HRD Government of India Delhi
4	Member	Shri. S. Goverdhan Rao Registrar National Institute of Technology Warangal


5	 Member	Prof. Y. Narasimhulu, Ph.D. Director (ASCI) University of Hyderabad Hyderabad
6	 Member	Prof. D V L N Somayajulu, Ph.D. Director Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh
7	 Member	Dr. D. Murali, Ph.D. Faculty In-charge (Accounts) Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh
8	 Special Invitee	Shri. A. Chidambaram Joint Registrar (Accounts) Indian Institute of Information of Technology Design and Manufacturing Kancheepuram Tamil Nadu








2.3 Building and Works Committee:

S. No.	Photographs	Description
1	 Chairman	Prof. D V L N Somayajulu, Ph.D. Director Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh
2	 Member	Prof. N. V. Ramana Rao, Ph.D Director National Institute of Technology Warangal





3	 Member	Dr. M. Nithyadharan Ph.D. Dept. of Civil Engineering Indian Institute of Technology Tirupathi
4	 Member	Prof. T D G Rao, Ph.D. Dept. of Civil Engineering National Institute of Technology Warangal
5	Member	Shri. G. Subrahmanyeswara Rao Executive Engineer, Tirupathi Central Division, Central Public Works Department
6	Member	Shri. K. Vasudevan Executive Engineer (Electrical) Vijayawada Division Central Public Works Department
7	 Member	Dr. K Eswaramoorthy, Ph.D. Convenor Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh

2.4 Senate:

S. No.	Photographs	Description
1	 Chairman	Prof. D V L N Somayajulu, Ph.D. Director Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh

2	 Member	Prof. N V S N Sarma, Ph.D. Director IIIT Srirangam Tiruchirappalli
3	 Member	Prof. P V Madhusudhan Rao, Ph.D. Department of Mechanical Engineering Indian Institute of Technology Delhi
4	 Member	Prof. Kamalakar Karlapalem, Ph.D. Professor International Institute of Information Technology Hyderabad
5	 Invitee	Dr. Sanjay Kumar Panda, Ph.D. Head, Dept of Computer Science & Engineering Indian Institute of Information Technology, Design And Manufacturing, Kurnool
6	 Invitee	Dr. Nitin Singh Singha, Ph.D. Head, Dept of Electronics & Communication Engineering Indian Institute of Information Technology, Design And Manufacturing, Kurnool
7	 Invitee	Dr. M Pulla Rao, Ph.D. Head, Dept of Mechanical Engineering Indian Institute of Information Technology, Design And Manufacturing, Kurnool
8	 Member	Dr. D. Murali, Ph.D. Faculty In-charge (Accounts) Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh

9	 Member	Dr. K Eswaramoorthy, Ph.D. Convenor Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh
10	 Member	Prof. P. Shankar, Ph.D. Dept. of Electrical Engineering, Indian Institute of Technology Delhi
11	 Member	Prof. C Krishna Mohan, Ph.D. Dept of Computer Science & Engineering Indian Institute of Technology Hyderabad
12	 Member	Prof. A Venu Gopal, Ph.D. Professor of Mechanical Engineering National Institute of Technology Warangal
13	 Member	Prof. V N Sastry, Ph.D. Professor Industrial Development and Research in Banking Technology Hyderabad
14	 Member	Prof. Vijay Kumar Gupta, Ph.D. Professor of Mechanical Engg. Indian Institute of Information Technology, Design And Manufacturing, Jabalpur
15	 Member	Prof. R B V Subramanyam, Ph.D. Professor of CSE National Institute of Technology Warangal

16	 Member	Dr. P Anjaneyulu, Ph.D. Infosys Technologies Ltd. Bangalore
17	 Member	Dr N Saratchandra Babu, Ph.D. Director SET Labs Chennai
18	 Member	Smt. Deepthi Lakkaraju Director Qualcomm Hyderabad
19	 Secretary	Prof. D V L N Somayajulu, Ph.D. Director Indian Institute of Information Technology Design and Manufacturing Kurnool, Andhra Pradesh

2.5 Anti-Ragging Committee:

1	Chairperson	Prof. DVLN Somayajulu, Director, IIITDM, Kurnool
2	Convenor	Prof. DVLN Somayajulu, Registrar I/C, IIITDM, Kurnool
3	Co-ordinator	Dr. Akhtar Khan, Assistant Professor, IIITDM Kurnool
4	Co-ordinator	Dr. B. Satya Sekhar, Assistant Professor, IIITDM Kurnool
5	Member	Dr. D. Murali, Assistant Professor, IIITDM Kurnool
6	Member	Dr. Sanjaya Kumar Panda, Assistant Professor, IIITDM Kurnool
7	Member	Dr. Nitin Singh Singha, Assistant Professor, IIITDM Kurnool
8	Member	Dr. Pullarao Muvvala, Assistant Professor, IIITDM Kurnool
9	Member	Dr. K.V. Eswaramoorthy, Assistant Professor, IIITDM, Kurnool
10	Member	Dr. Ravinder Katta, Assistant Professor, IIITDM Kurnool

11	Member	Dr. Situ Rani Patre, Assistant Professor, IIITDM Kurnool
12	Member	Dr. Mohamed AsanBasiri M, Assistant Professor, IIITDM Kurnool
13	Member	Dr.Renjith P, Assistant Professor, IIITDM Kurnool
14	Member	Dr.Srilakhmi R, Assistant Professor, IIITDM Kurnool
15	Member	One Representative from District Admin
16	Member	One Representative from Police Admin
17	Member	One Representative from Local Media
18	Student Member	Mr. Piyush Raote (Roll No.: ME17B0034)
19	Student Member	Ms. Divya Srivastava (Roll No.: COE17B008)






2.6 Internal Complaints Committee (ICC) under Sexual Harassment of Women at Workplace

1	Chairperson	Dr. Situ Rani Patre, Assistant Professor, IIITDM, Kurnool
2	Member	Dr. R Srilakshmi, Assistant Professor, IIITDM, Kurnool
3	Member	Ms. Pranava Devi, IIITDM Kurnool
4	External Member	Dr. A Vimala Rodhe, Head, Microbiology, Silver Jubilee College
5	Member	Dr. Akhtar Khan, Assistant Professor, IIITDM, Kurnool

3. STAFF DETAILS

3.1 Teaching staff:







a) Department of Computer Science & Engineering:

S. No.	Photographs	Description
1		Dr. D.V.L.N. Somayajulu, (Ph.D., IIT Delhi) Professor & Director Areas of Interest: Databases, Information Extraction, Query Processing, Big Data and Privacy
2		Dr. Sanjaya Kumar Panda (Ph. D., IIT Dhanbad) Asst. Professor & Head Areas of Interest: Cloud Computing, Big Data Analytics and Recommender Systems
3		Dr. Renjith P. (Ph.D., IIITDM Kancheepuram) Asst. Professor Areas of Interest: Graph Theory, Graph Algorithms
4		Dr. Biswajit R Bhowmik (Ph.D., IIT Guwahati) Asst. Professor (On Contract) Areas of Interest: Network-on-Chip: 2D, 3D, Wireless, and Photonic, VLSI Testing, Formal Verification, High Performance Computing, Cyber Physical Systems, Distributed Computing Systems.
5		Dr. P Pavan Kumar (Ph.D., University of Hyderabad) Asst. Professor (On Contract) Areas of Interest: Network-on-Chip: 2D, 3D, Wireless, and Photonic, VLSI Testing, Formal Verification, High Performance Computing, Cyber Physical Systems, Distributed Computing Systems.



b) Department of Electronics & Communication Engineering

S. No.	Photographs	Description
1		Dr. Nitin Singh Singha (Ph. D., IIT Kanpur) Asst. Professor & HoD Areas of Interest: Game Theory, Blockchain, Peer-to-Peer Networks, Social Networks
2		Dr. Mohamed Asan Basiri (Ph.D., IIITDM Kancheepuram) Asst. Professor Areas of Interest: VLSI for Signal Processing, VLSI for Information Security
3		Dr. Eswaramoorthy K V (Ph.D., IISc Bangalore) Asst. Professor Areas of Interest: Non-invasive monitoring of body fluids, Electrochemical biosensor and gas sensor, Biomedical Instrumentation & Industrial Automation, Internet of Things (IoT) for Agriculture, manufacturing industry and Smart City
4		Dr. Situ Rani Patre (Ph. D., IIT(BHU), Varanasi) Asst. Professor Areas of Interest: Broadband, UWB, Frequency Independent Antennas, Reconfigurable and MIMO Techniques, Metasurfaces, RF Energy Harvesting.
5		Dr. Pallab Kumar Nath (Ph.D., IIT Kharagpur) Asst. Professor (On Contract) Areas of Interest: VLSI Architecture Design for Image and Video Processing Algorithms, VLSI Architecture for DSP, FPGA Based Embedded System design, Biomedical Instrumentation
6		Dr. Satyendra Singh Yadav (Ph.D., NIT Rourkela) Asst. Professor (On Contract) Areas of Interest: Wireless Communication, 5G systems, Energy Harvesting for IoT, GPU Computing.
7		Dr. Debajit De (Ph.D., NIT Rourkela) Asst. Professor (On Contract) Areas of Interest: Microstrip & Planner Antenna Design, Antenna Theory and Techniques, Microwave Engineering, Radio Frequency Circuit Design, Electro-magnetic and RF MEMS.

c) Department of Mechanical Engineering:

S. No.	Photographs	Description
1		Dr. Pullarao Muvvala (Ph.D., IIT Madras) Asst. Professor & HoD Areas of Interest: Heat Transfer and Fluid Flow (Experimental and Computational), Electronic cooling, Optimization studies
2		Dr. Maniprakash S (Ph.D., TU Dortmund, Germany) Asst. Professor Areas of Interest: Continuum Mechanics, Constitutive Modelling, Smart Materials
3		Dr. Akhtar Khan (Ph.D., NIT Rourkela) Asst. Professor Areas of Interest: Machining of “difficult-to-cut” materials, Machine Tool Technology, Optimization Methods in Engineering Design (Single and Multi-Objective), Design of Experiments, Multi-Criteria Decision Making.
4		Dr. B. Sandeep Reddy (Ph.D., IISc Bangalore) Asst. Professor (On Contract) Areas of Interest: Nonlinear Dynamics of Mechanical Systems, Robotics and Control, Applied Dynamics
5		Dr. Bhogilla Satya Sekhar (Ph.D., IIT Guwahati) Asst. Professor (On Contract) Areas of Interest: Hydrogen Energy, Solid State hydrogen storage, Thermal Energy Storage.
6		Dr. R. Srilakshmi (Ph.D., IIT Hyderabad) Asst. Professor (On Contract) Areas of Interest: Finite element analysis, Damage mechanics of composites, Computational Fracture mechanics.

d) Department of Sciences:

S. No.	Photographs	Description
1		Dr. D. Murali (Ph.D., IGCAR, Kalpakkam) Asst. Professor & HoD Areas of Interest: Computational condensed matter, ab-initio electronic structure calculations, Photovoltaic effect in perovskite based solar cells, phonon transport, solid oxide fuel cells, nanostructure evolution in structural materials
2		Dr. Ravinder Katta (Ph.D., IIT Roorkee) Asst. Professor Areas of Interest: Mathematical Control Theory, Inverse Problems, Ill posed operator equations and Regularization Theory.

3.2 Non-teaching staff:

1	Mr. C Sreenath Reddy , M.Tech Network Administrator (On Contract) Dept. of Computer Science & Engineering
2	Mr. Rajasekharan , M.Tech Lab – Technician (On Contract) Dept. of Electronics & Communication Engineering
3	Mrs. M Radhika , M.Tech Lab – Technician (On Contract) Dept. of Electronics & Communication Engineering
4	Mr. S Srinivas , B.Tech Lab – Technician (On Contract) Dept. of Electronics & Communication Engineering
5	Mr. P Ramanjaneyulu , B.Tech Lab – Technician (On Contract) Dept. of Mechanical Engineering
6	Mr. D Akhil Kumar , B.Tech Lab – Technician (On Contract) Dept. of Mechanical Engineering
7	Mr. D Jithendra , B.Tech Lab – Technician (On Contract) Dept. of Mechanical Engineering
8	Mr. G Sarat Kumar , MBA Office Assistant (On Contract)
9	Mrs. G Pranava Devi , MBA Office Assistant (On Contract)
10	Mr. D Anjaneyulu , M.Com Office Assistant (On Contract)
11	Mr. Bhargav Guptha , B.Com Office Assistant (On Contract)
12	Mr. G Murali Krishna , M.Sc (Library & Information Sciences) Librarian (On Contract)

4. ACADEMIC PROGRAMMES:

This section provides details about the undergraduate programmes offered, along with their year wise Enrolment with sex, caste break-up, admission statistics, student's total strength, scholarships/monetary assistance and examination results.

4.1 Bachelor's programmes

4.1.1 B. Tech in Computer Engineering

B. Tech. in Computer Science and Engineering curriculum is modeled on the ACM (Association for Computing Machinery) recommendations and is the first of its kind engineering program offered in India. This program is aimed at producing engineers equipped with skills required for efficient hardware-software interaction. The program encompasses a variety of topics related to computation, analysis of algorithms, programming languages, program design, software, and computer hardware. In addition to courses offered by the conventional Computer Science curriculum, this novel program offers core courses such as Embedded Systems, Human-Computer Interaction, Simulation and Modelling, Signals and Systems, Product Design, etc., that equip the students with both computing and electronics engineering skills that are very much required for the successful creation of products requiring hardware – software interactions. Our graduates would find wide scope in VLSI, Embedded Systems and Electronics Product Manufacturing related industries in addition to application development avenues and higher studies that are open to conventional Computer Science engineers.

4.1.2 B. Tech in Electronics and Communication Engineering with specialization in Design and Manufacturing

Today's electronic product design and development requires skilful blend of expert hardware and software engineering together with a spirit of creativity and innovation that is also tempered by the practical concerns of manufacturability, cost consciousness and reliability. The Electronics and Communication Engineering with specialization in Design and Manufacturing curriculum is designed to provide advanced theoretical and practical training of all aspects relevant to the design, development, and production of modern electronic systems and subsystems. The Electronics and Communication Engineering with specialization in Design and Manufacturing (EDM) program prepares you for a wide range of engineering study and career options,

including business, Biomedical Engineering, Computer Hardware, Aerospace Industry, Computer Software, Nanoelectronic chips, Photonics, Nanoengineering, Robotics, and Solar Energy Harvesting and Distribution.

4.1.3 B. Tech in Mechanical Engineering with specialization in Design and Manufacturing

Mechanical Engineering with specialization in Design and Manufacturing (MDM) offered by IIITDM Kurnool augments the existing Mechanical Engineering curricula offered by IITs by offering design courses on conceptualization, visualization, and engineering simulations. Equipped with well-structured instruction and learning resources and research facilities, the institute aims to disseminate education in the inter-disciplinary areas of design and manufacturing engineering.

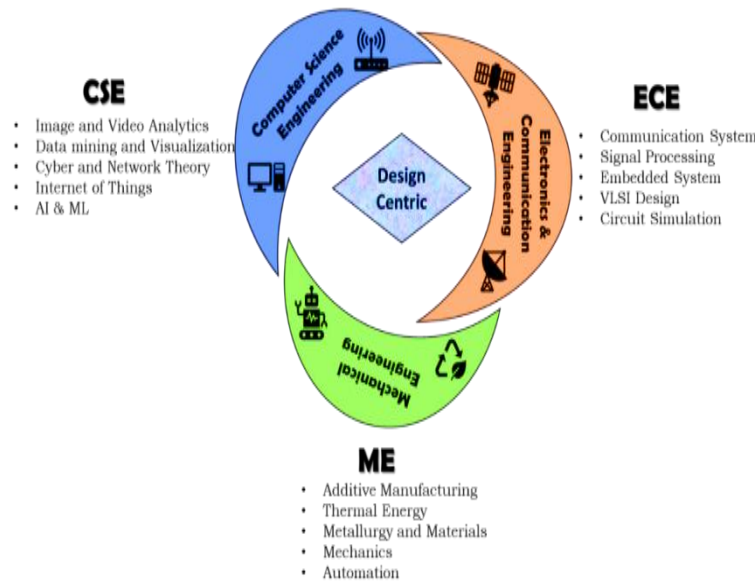
Design visualization imparted through graphic art practice and product design practice enables students to conceptualize, design, simulate and develop tangible products. Students undergo interdisciplinary courses such as embedded systems, instrumentation, controls, automation and advanced manufacturing technology that will help them to design and develop innovative engineering products. Students can choose courses among electives and pursue their interests. The program offers a blend of courses that impart knowledge on design thinking and interdisciplinary engineering in addition to basic sciences.

4.1.4 Design & Innovative Centric Engineering Curriculum

The institute is currently offering design centric undergraduate programmes in the following three disciplines with intake of 40 students in each programme. In the Academic year 2018-19, the total intake for the undergraduate programmes is 120.

S. No.	Name of the Undergraduate Programme	Starting Year	Intake
1	B. Tech. in Computer Engineering	2015	40
2	B. Tech. in Electronics and Communication Engineering with specialization in Design and Manufacturing	2015	40
3	B. Tech. Mechanical Engineering with specialization in Design and Manufacturing	2016	40

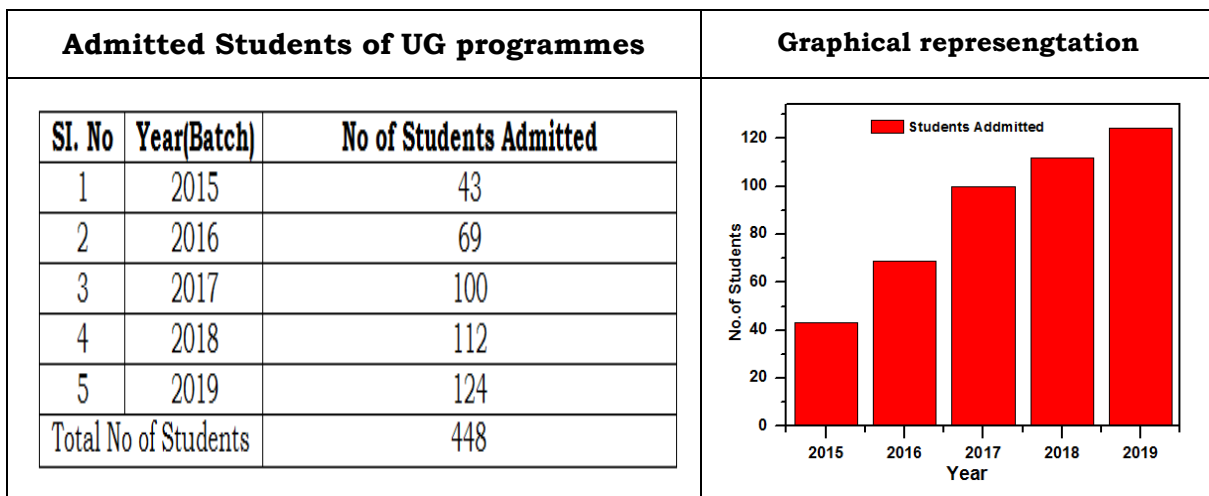
The following figure shows overview of theme of all the three undergraduate programmes offered at this institute



Theme of Undergraduate Programmes

4.2 Admission Statistics

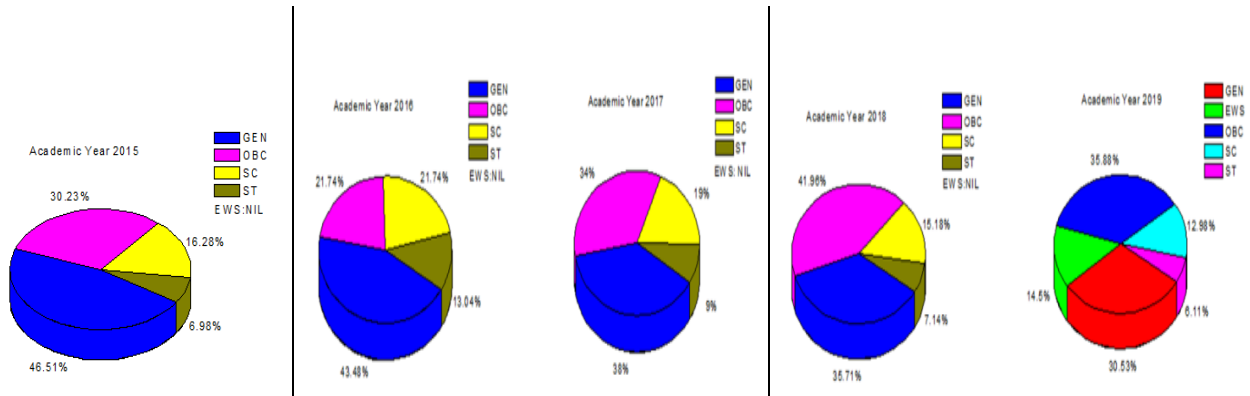
Details of statistics of admitted students along with male-female ratio and category wise admitted students of all the offered programmes are given below:



Category wise Statistics

SI. No	Batch	General	EWS	OBC	SC	ST
1	2015	20		13	7	3
2	2016	30	---	15	15	9
3	2017	38	---	34	19	9
4	2018	40	---	47	17	8
5	2019	35	19	40	20	10
Total		143	19	136	71	36

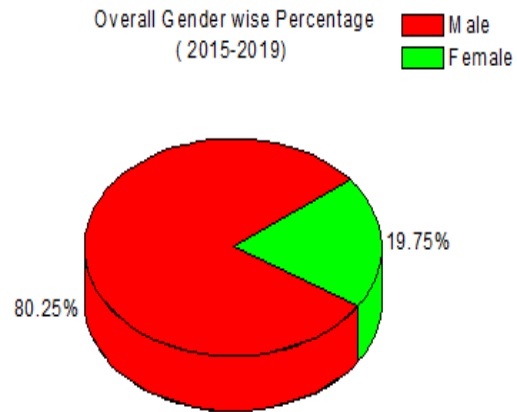
Graphical Representation of GEN, OBC, SC and ST



Gender wise Statistics

SI. No	Batch	Male	Female
1	2015	29	14
1	2016	51	18
2	2017	77	23
3	2018	92	20
4	2019	105	19
Total		325	80

Graphical Representation



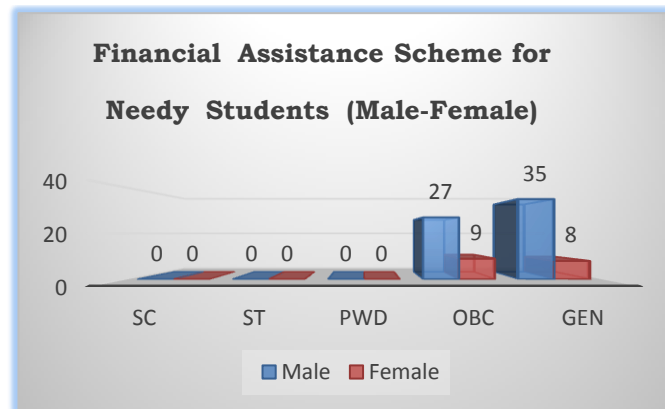
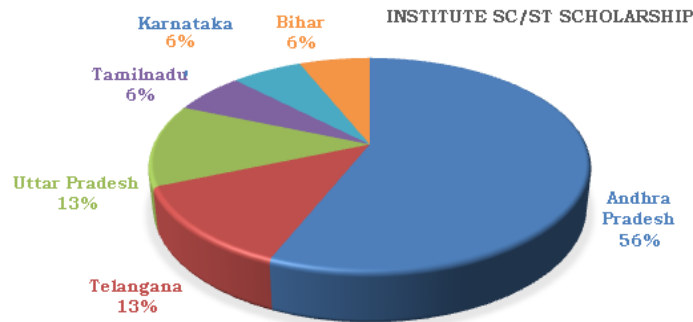
5 SCHOLARSHIPS

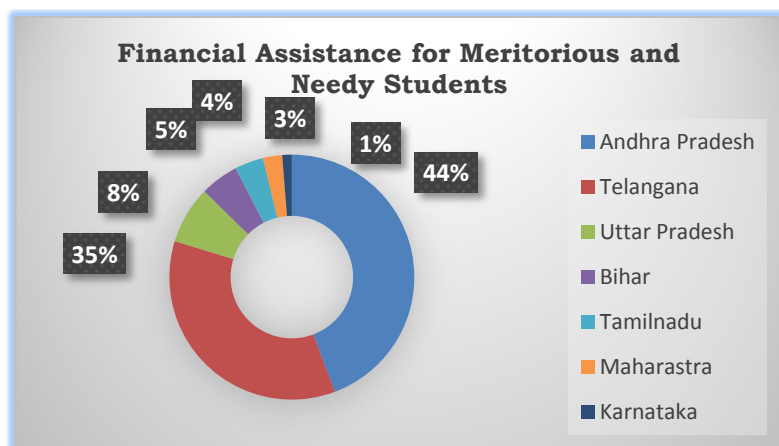
The institute has made every effort to fill up the seats reserved for SC/ST /OBC/PWD/GEN candidates fully since the inception. These efforts included fee concession in payment of registration fee, and relaxing the minimum eligibility requirements in admissions.

For all admitted students belonging to SC/ST community, tuition fee is waived. For needy SC/ST students, **institute scholarship** was given under which hostel seat rent was waived along with free mess (basic menu only) and a monthly pocket allowance of Rs. 250/- was given to all the students whose parents annual income is less than or equal to Rs.4,50,000/-.

Following are the details of beneficiaries of institute scholarship in the Academic Year 2018-2019.

S. No.	Scholarship Name	SC		ST		PWD		OBC		GEN	
		Male	Female	Male	Female	Male	Female	Male	Female	Male	Female
1.	Institute Scholarship for SC/ST needy students	13	0	3	0	0	0	0	0	0	0
2.	Financial Assistance Scheme for Meritorious and Needy Students (Merit cum Means Scholarship)	0	0	0	0	0	0	27	9	35	8





Institute has also made full effort for implementing central sector scholarship schemes for SC/ST/OBC/PWD/EWS students. Following are the details of beneficiaries of the Central Sector Scholarships in the academic year 2018-19.

S. No.	Name of the Central Sector Scholarship Scheme	Number of Students to whom scholarship was sanctioned	SC		ST		PWD		OBC		GEN	
			Male	Female	Male	Female	Male	Female	Male	Female	Male	Female
1.	National Fellowship and Scholarship for Higher Education of ST Students - Scholarship (Formally Top Class Education for Schedule Tribe Students)	17	0	0	12	5	0	0	0	0	0	0
2.	Central Sector Scholarship of Top Class Education for SC Students	10	7	3	0	0	0	0	0	0	0	0
3.	Post Matric Scholarship for Students with Disabilities	3	0	0	0	0	2	1	1	0	1	1
4.	Central Sector Scheme of Scholarships for College and University Students	3	0	0	0	0	0	0	1	0	1	1
5.	Post Matric Scholarship Schemes Minorities CS	1	1	0	0	0	0	0	0	0	0	0
6.	Financial Assistance for Education to the Wards of Beedi/Cine/IOMC/LSDM-Post Matric	1	0	0	0	0	0	0	1	0	0	0
7.	MukhyamantriMedhavi Vidyarthi Yojna (MMVY) scholarship from the Govt. of Madhya Pradesh	2	0	0	0	0	0	0	2	0	0	0

The institute has also initiated and made a full effort to facilitate sanction of state government scholarships offered by disparate states to our students.

6 RESEARCH AND DEVELOPMENT ACTIVITIES

6.1 Research publications

Faculty of this institute have published 3 journal papers and 7 conference papers during 2018-19.

Journals:

1. Biswajith Bhoumik, Santhosh Biswas, Jatindra Kumar Deka, Bhargab B, Bhattacharya, **“Performance Aware Test Scheduling for diagnosing co-existent Channel Faults in Topology- Agnostic Network-on-Chip”** ACM Transaction on Design Automation of Electronic Systems (ACM, TODAES), Vol. 24 No. 2, Article 17, Pages: 17:1-17-29, 2018.
2. K Kaur, D Murali, BRK Nanda, **“Stretchable and Dynamically Stable Promising Two-Dimensional Thermoelectric Materials: ScP and ScAs”**, J. Mater. Chem. A, 7 12604 (2019).
3. B. Satya Sekhar, H. Niyas, **“Design of a Hydrogen Compressor for Hydrogen Fueling Stations”**, Int. J. Hydrogen Energy 2019.

Conferences:

1. Biswajith Bhoumik, Santhosh Biswas, Jatindra Kumar Deka, Bhargab B, Bhattacharya “Locating Open-Channels in Octagon Network on a Chip Microprocessor”, IEEE 10th Latin America Symposium on Circuits and Systems Feb, 24-27, 2019, Colombia Latin America.
2. B. Sathya Sekhar, “Design of a Hydrogen Compressor for Hydrogen Fueling Station” 16th International Symposium on Metal Hydrogen System, Oct 28th – 2nd Nov 2018 Guangzhou, China.
3. B. Sathya Sekhar, “Thermal Design of Hydrogen Storage Tank for Renewable Energy Applications”, 12th International Symposium on Hydrogen and Energy, Feb 11-16th 2018.

4. Biswajith Bhoumik, “A Time-Optimised Test-Solution Scheme for the Analysis of permanent Faults on NoC Interconnects”, IEEE 8th International Symposium on Embedded Computing and System Design, Dec, 13-15th 2018
5. Biswajith Bhoumik, “Heaping of Sorrow Upon Sorrow” 4th International Symposium on Smart Electronic Systems, Dec, 17-19th 2018, Hyderabad, India.
6. Mishra R., Barnwal S.K., Malviya S., Mishra Prasoon, Tiwary U.S., "Prosodic Feature Selection of Personality Traits for Job Interview Performance". International Conference on Intelligent Systems Design and Applications Advances in Intelligent Systems and Computing, pp 673-682.
7. A Concha, EK Varadharaj, NM Hernandez-Rivera, SK Gadi, A novel implementation technique for genetic algorithm-based auto-tuning PID controller, 2017 IEEE International Conference on Power, Control, Signals and Instrumentation Engineering (ICPCSI), 1403-1408.

6.2 Research projects

A collaborative research project entitled, **“Development of 3D Printed Wearable Button Antenna for Soldier Performance”** is accepted for the research grant by IMPRINT-2, DST, GoI. The Co-PI of this project is Dr. Eswaramoorthy KV, Assistant Professor, Indian Institute of Information Technology, Design and Manufacturing, Kurnool.

6.3 Research guidance

In the academic year 2018-19, the institute has started Ph.D. programme during May 2019 and a total of Four (04) Ph.D. students were selected in the Departments of Mechanical Engineering and Electronics & Communication Engineering. These scholars will be admitted in the Institute during the July 2019-20 session.

6.4 Innovation

An Innovation centre has been established in the month of April 2019. The Vision of this centre is to build an academic and research eco system with the innovative mind-set, for creating industry-ready professionals, entrepreneurs, and researchers in multi-disciplinary environments. The Mission is to focus on Innovative

design and smart manufacturing of the products for the industry and society by leveraging the innovation in the emerging technologies.

As per the institute Vision and Mission, the Innovation road map was identified based on brain storming session among the stake holders with the assistance from Industry expert(s) and identified the following FOUR Objectives:

- i. First Objective** is to trigger Innovation mindset among the Faculty and Students to explore the frontiers beyond the Curriculum and Syllabus. Tech talks and Workshops were organised towards this goal.
- ii. Second Objective** is to set up Centre of Excellences (CoEs) in the emerging areas such as Industry 4.0, IoT, Electric Vehicles. Plans are there to provide Technical trainings in these respective topics by bringing in Industry experts. Teams are formed in each of these CoEs to bring all stake holders into one platform and getting them ready to address the Industry needs and problems.
- iii. Third Objective** is by using CoE as foundation, to set up Research & Consulting group to take up the challenging Research and Development (R&D) work. Two MoUs are signed towards this goal.
- iv. Fourth Objective** is to set up Technology Incubation & Entrepreneurship (TIE) centre in the campus for the students to experience Entrepreneurship mind set and aid them to be future successful entrepreneurs and contribute back to the society.

Institute is planning to set up a TIE Centre by seeking support from MeitY as part of TIDE 2.0 Program. In addition, the institute has identified a dedicated Faculty-in-Charge, to draw the strategy, connect with the Industry and implement all the above four Goals, by leveraging the strengths of the Faculty and Students.

7 LABORATORIES

7.1 Mechanical and Manufacturing Laboratory

The objective of the manufacturing laboratory is to provide a comprehensive understanding in the field of conventional (traditional) machining processes, creating an emphasis on the techniques adopted in the industries. The laboratory holds equipment ranging from conventional numerically controlled lathe machine, which can be availed for internal and external threading operations for disparate machining parameters and respective values.



Numerically Controlled Lathe Machine



Drilling Machine



Grinding operation: Abrasive Wheel



Pocket Drill

7.2 Thermal Laboratory

The traditional research areas in the area of thermodynamics, heat transfer, fluid dynamics, turbulence, multiphase flow and basic combustion play a key role in understanding a problem which affects the macro and micro scale of objects. An improvement of any mechanical system is done with the disparate studies of fluid mechanics and heat transfer pertaining to the system. A fundamental component and in brief learning of turbulence (which is classified in terms of flow laminar, turbulent, non-linear leads to the motion of fluids and thermal fields affecting them) is challenging in every aspect of engineering divisions.

Thermo-fluids research covers a broad range of components, in fundamental as well as applied subjects. The broad spectrum ranges from the topics of heat flow, turbulence, multi-phase models (reacting models included), hydrodynamics and atmospheric datum flow. In fluid mechanics, disparate equipments ranging from solar harvesting rig, refrigeration (air conditioning) tutor, wind tunnel experiments, diesel cycle study a synergy is created between fluid mechanics and heat transfer laboratory as a combination of study and numerical modeling of the complex engineering task. This synergy created helps the students in understanding the environmental systems and also as they progress they tend to develop advanced tools which can be predictive in nature by adopting interdisciplinary research. This abridgment of gap of disparate branches of engineering is the focus point of our work in Indian Institute of Information Technology, Design and Manufacturing, Kurnool. This leads to erasing of boundaries each engineering branch and leads to one module of work corresponding to the engineering spectrum.



Combined Heat Exchanger Setup



Friction in Pipe Flow



Air Conditioning Tutor



Free and Forced Convection; Thermal Conductivity of Non Metallic Materials

7.3 Mechanical Design Laboratory

A branch of applied science which highlights the relationship between geometry and relative motion of the parts of the machine in consideration is broadly classified in the field of design practice laboratory.



Journal Bearing Apparatus



Critical Speed Apparatus



Balancing Apparatus



Motorized Gyroscope Apparatus



Universal Vibration Setup

The Design Laboratory consists of theory of machines where the laboratory equipment is utilized to equip students about basics of machine engineering (motion) to advanced field of studies which corresponds to free and forced vibration entity, friction in bearing, geared system and governors. Wide range of equipment are available ranging from static and dynamic balancing equipment which is used to study the balance of masses statically and dynamically of a single rotating system (observation is to find the effect of unbalance in the rotating mass), motorized gyroscopic is used to study the gyroscopic effect of a rotating disc, the gyroscopic effect of a rotating disc, the universal vibration setup provides a comprehensive unit to perform the vibration experiments, the universal frame present in the laboratory facility is quick to assemble and can be modified based on the experiment performed by the students ranging from simple relation of pendulum, radius of gyration (compound pendulum, b-filar suspension), undamped free vibration of spring mass

system, longitudinal vibration of helical coiled spring, torsion, damping coefficients, forced damp system, etc. A cam analysis equipment is present to study displacement vs. angle of rotation, follower weight on bounce and also to study the effect of compression (spring) bounce. The journal bearing setup is provided, which is used to study the pressure profiles of lubricating oil at various conditions of load and speed, plotting the Cartesian polar pressure curves, and to measure the frictional torque and power transmit. Apart from this in Design laboratory, students perform Industrial sketching, Modelling of objects and analysis of materials using Sketching Tool, AutoCAD, MATLab and ANSYS tools. This helps the students to pro-actively work in real time modelling problems and helps them attain knowledge in a wide database of framework.







7.4 VLSI Laboratory:

VLSI Lab is highly equipped with upto date industry standard VLSI Tools and hardware resources. The lab facility includes course lab which provides projects and assignments for VLSI design and synthesis. The VLSI lab implements the theoretical concepts studied as part of subjects CMOS VLSI Design, Microelectronics Circuits and Verilog, for students to experience in practical with the help of Xilinx Vivado and LTSpice.

The lab introduces a complete custom IC design flow, ASIC design flow and AMS (Analog and Mixed Signal) flow for Analog circuits, Digital circuits and Analog and mixed signal circuits are designed respectively. The analog design involves schematic (standard cell), test schematic capture and symbolic representation of circuit topologies using LTSpice. Simulation of the test circuit to perform various analyses such as transient, DC and AC is facilitated.

The digital design involves the realization of various digital circuit components using Register Transfer Logic (RTL) code, Compilation of the same using Xilinx Vivado, The synthesis of the verified RTL code to obtain the gate level netlist is performed thereon. Synthesizing the design (Synthesis, netlist generation, place and route etc..) in to output files that FPGAs can understand and program the output file to the physical FPGA device (ZedBoard) using the available programming tools is done. In the same facility embedded systems, microprocessors and controllers, communication systems and digital signal processing lab is carried out. This shows multi facility equipped laboratory for amalgamation of students learning.


S. No	Equipment	Images of the equipment	Description
1	ZED BOARD - 7000		<ol style="list-style-type: none"> 1. Can switch the between the two video inputs or different video formats. 2. Maximum input and output resolution 2048pixel to 2048 pixel. 3. Real time scale upto 64X. 4. Built in YCrCb to RGB converter, YUV to RGB. 5. Converter and RGB to YCrCb converter.
2	SPECTRUM ANALYSER		<ol style="list-style-type: none"> 1. 1.9 kHz - 6.2 GHz frequency range 40 MHz real time bandwidth External reference and trigger/sync inputs USB3.0 2. Power/control/data interface to PC Publicly-accessible software application programming interface (API) for Windows and Linux operating systems 3. RSA306B-SMA model provides a SMA connector. 4. RSA306B-SMA with the




			No-Shell option ships without the plastic housing, allowing, integration
3	8086 MICROPROCESSOR		<ol style="list-style-type: none"> 1. INTEL 8086CPU AT 4.77 MHZ CLOCK SPEED. 2. 16KB for monitor EPROM upgradable to 64 KB. 3. 16KB RAM expandable to 64KB. 4. Battery backup provision for RAM upto 64KB compatible keyboard 5. 24 TTL I/O lines brought out to two nos., of 26 pin FRC connector number of standard RS232C compatible serial port brought out to a pin D type male connector 6. 3 channel 16 bit counter/timer using 8253 7. 8 numbers of interrupt lines are terminated at a 10 pin connector. 8. Kit operates with a single +5V/DC supply 9. Built-in line assembler & Disassembler.
4	ARM- LPC 2148 KIT		<ol style="list-style-type: none"> 1. 16-bit/32-bit ARM7TDMI-S microcontroller in a tiny LQFP64 package. 2. 8 kB to 40 kB of on-chip static RAM and 32 kB to 512 kB of on-chip flash memory. 3. 128-bit wide interface/accelerator enables high-speed 60 MHz operation.

5	TIVA C SERIES TM4C123G		<ol style="list-style-type: none"> 1. Frequency-80 MHz 2. 32-bit ARM dual 12 bit ADC. 3. 256 kb flash /32bit Kbsrm/ 2 Kbeeprom.
---	---------------------------	---	--

7.5 Digital Logic Design Laboratory:




The Digital Logic Design Lab (DLD Lab) is one of the most important and well-equipped lab of the Department. This lab is re-designed such that the students get an opportunity to learn across the course regarding Digital systems course. This is an undergraduate course which deals with the basics of digital systems design. It provides the prerequisites for advance courses in digital electronics. Because of the significance of this course the DLD Lab has been carefully designed to meet the course requirement. Analog Circuit Laboratory is also conducted in the DLD lab facility were, disparate analog circuits are designed (Amplifiers, Filters, Oscillators). The Analog electronic circuit includes an analog signal with any continuously changeable signal. While working on an analog signal, an analog circuit alters the signal in some manner. Analog circuit can be used to convert the original signal into some other format such as a digital signal.





S. No.	Equipment	Images of the equipment	Description
1	DIGITAL TRAINER		<ol style="list-style-type: none"> 1. On-Board Digital input (switches). 2. BCD to seven segment. 3. IC 555 timer, Edge trigger. 4. IC 74121 Multivibrator 5. On-Board Bread Board for external circuits. 6. LED Output indication.

2	FUNCTION GENERATOR		<ol style="list-style-type: none"> 1. Dual-channel, 25 MHz or 60 MHz sine waveforms. 2. 12.5 MHz or 30 MHz square waveforms 3. 14 bits, 125 MS/s or 300 MS/s arbitrary waveforms with 8 k points or 1 M points record length. 4. Amplitude 1 mVp-p to 10 Vp-p into 50 Ω loads.
3	DIGITAL STORAGE OSCILLOSCOPE		<ol style="list-style-type: none"> 1. Dual time base Math Fast Fourier Transform (FFT) 2. Pulse Width trigger capability 3. Video trigger capability with line-selectable triggering 4. External trigger Setup and waveform storage 5. Variable persistence display 6. RS-232, GPIB, and Centronics ports with the optional TDS2CMA 7. Communications Extension Module
4	REGULATED POWER SUPPLY		<ol style="list-style-type: none"> 1. 3 channel DC supply. <ul style="list-style-type: none"> i)dc supply of 15V (variable). ii) 5V(fixed). iii)30V (Variable).

7.6 Electrical Drives and Sensor Instrumentation Laboratory

Current Sensing of Electrical Drives is required for the implementation of current limit control, inner current control loop of closed-loop speed control, closed-loop torque control of a dc drive, for sensing fault conditions, and for sensing speed in dc drives by back emf sensing method. In order to avoid interaction between control circuit, carrying low voltage and current, and power circuit involving high voltage and current and sometimes harmonics and voltage spikes, isolation must be provided between the two circuits.

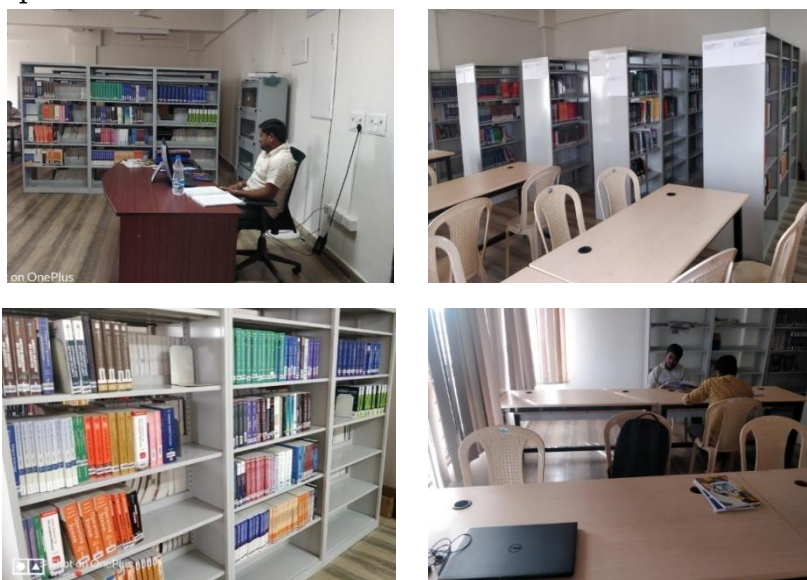
S. No.	Equipment	Images of the equipment	Description
1	MIXED DOMAIN OSCILLOSCOPE		MODEL NO: MDO36024, 4 CHANNEL NON ISOLATED, 200MHZ, 2.5Gs/s
2	CURRENT PROBE		MODEL NO: N2783B, 30A/100MHZ
3	EMC PROBE SET(4-PIECES)		MODEL NO: TBPS01, LESS THAN 75V DC OR AC (UPTO 50V)

4	20dB WIDEBAND AMPLIFIER		MODEL NO:TBWA2_20, 20dB
5	VOLTAGE PROBE (PERIPHERAL FOR MDO)		MODEL NO:TPP0250, 250MHZ,300V
6	LINE IMPEDANCE STABILISATION NETWORK LISN		MODEL NO:TB0H01, 5uH
7	LAPTOP		MODEL NO:15g-br001tu, intel i3 processor,1TB HD

8 CENTRAL FACILITIES AND SERVICES:

8.1 Central Library

Central Library is one of the central support services of Indian Institute of Information Technology, Design and Manufacturing, Kurnool. The mission of the library is to provide quality information and services to all students, faculty and staff. It provides a high-quality ambience for both thoughtful and collaborative workplace for study. The Library uses an automated (Library and Information Management) software KOHA with Integrated RFID Technology. All the registered users can access the Institute library anywhere within the campus through LAN/Wi-Fi. The Library has an excellent collection of books, printed journals, magazines, leading newspapers, e-Journals, soft copy of NPTEL course materials and video contents. The Library maintains a separate collection of reference books.



8.2 ATM Facility

ATM Facilities in IIITDM Kurnool are extended by STATE BANK OF INDIA through its Branch that is located in the city premises which is 4 km away from Institute. ATM facility, Internet Banking, Tele-banking are available in the campus for the benefit of students and staff.

Branch: SBI Nandyal Road (IFSC Code: SBIN0021660)

Contact Number: 08518-274441

Timings: 10.30 am - 4.30 pm (Monday - Saturday) (*II and IV Saturday off)

ATM: 24*7 hours ATM is situated in the campus.



8.3 Health centre

The Health Centre (24 x 7) inside the campus provides basic health care to all the students, faculty and staff. The Institute also has a tie-up with a KIMS hospital, Kurnool to provide medical care, which also supports an ambulance service (24 x 7) to attend any emergencies. One doctor will be available from 5 PM to 8 PM. All the students are covered under group medical insurance arranged by the Institute.



8.4 Canteen:

The College has a well-established canteen for refreshments. It serves hot and cold beverages like Tea, Coffee and cold drinks from 8.00 AM to 8.00 PM on all working days including Saturdays. With seating capacity of 60 members it allows students and faculty enough space along with delicious lunch and breakfast at nominal prices. It has a good variety of snacks, chocolates, ice creams etc.



9 NOTABLE ACHIEVEMENTS

9.1 MoUs signed

- a) IIITDM Kurnool has signed MoU with CodeTantra on 11th March 2019 for providing the online platform to students to get exposed and learn programming languages such as C, C++, JAVA, and hadoop.
- b) IIITDM Kurnool has signed MoU with Centilian Networks Pvt. Ltd to impart training, consultancy, and research activities on Drones, Robotics, and Artificial Intelligence for the students and staff.

9.2 Student and faculty Achievements:

- a) Mr. A. Sai Kaushik, final year Mechanical Engineering student under the mentorship of Dr. B. Satyasekhar, has received best paper award for the paper entitled, "Thermal Integration of Proton Exchange Membrane (PEM) Fuel Cell with Recuperative Organic Ranking Cycle", 11th International Exergy, Energy, and Environment Symposium (IEEEES-11), Chennai, July 2019.
- b) Dr. B. Satya Sekhar received Department of Science and Technology Travel grant award for attending 16th International Symposium on Metal-Hydrogen Systems held in Guangzhou, China.

9.3 Faculty Development Programmes:

- a) One-week Faculty Development Programme on "**Outcome Based Education**" was organized by IIITDM Kurnool in association with E & ICT Academy, NIT Warangal from May 6 -11, 2019. The sessions were handled by eminent speakers Prof. DVLN Somayajulu (Director, IIITDM Kurnool), Prof. SDK Mandal (CET, IITKGP), Prof. IAK. Reddy (TLC, NIT Warangal), Prof. A. Venu Gopal (NIT Warangal), Prof. K Srinivas (Head ICT, NIEPA) and Prof. Y Narasimhulu (University of Hyderabad). The FDP programme received an overwhelming response with 65 participants from various engineering and degree colleges in the state of Andhra Pradesh. Mr. D Nagendra Kumar, DIG, Kurnool Range, Andhra Pradesh and Prof. N V Ramana Rao, Director of NIT Warangal were the chief guests for the Inaugural and Valedictory functions respectively.



9.4 Guest Lectures

- a) Dean of Faculty Affairs, IIITDM Kancheepuram, Dr. Sreekumar delivered a Guest lecture on "Robotics and its future"
- b) Dr. Suresh Varadarajan delivered a Guest lecture on "Data Analytics" and "Internet of Things (IoT)" at Indian Institute of Technology, Design and Manufacturing, Kurnool.



Lecture on "Robotics and its future"



Lecture on "Data Analytics" and "Internet of Things (IoT)"

9.5 Competitions

Indian Institute of Technology, Design and Manufacturing, Kurnool organized the Zonal Round Workshop & Competition of IoT Challenge 2019 in collaboration with Indian Institute of Technology, Bombay and i3indya Technologies.



10 TECHNICAL ASSOCIATION AND STUDENT HOBBY CLUBS

10.1 Student clubs:

The institute has constituted various activities based clubs and associations to monitor and perform various extra-curricular and curricular activities. Different clubs that are available in the institute are as follows:

- a) **Student Activity Council (SAC)**
- b) **Social Service Group (SSG)**
- c) **Cultural Club** Conducts activities such as Dance, Music, Drama, Art,
- d) **Photography clubs to capture** events like national days, fresher night, Deepawali, nature, development of our campus etc.
- e) **Sports Team:** Many tournaments like speed cuber, rubik's cube, chess, ludo, carom, table tennis were conducted in the hostels. Sports team from various backgrounds have also participated in annual inter IIIT sport meet.
- f) **Electronics-Mechanical-Computer (EMC) Club:** This is formed by students of Electronics and Communication Engineering. The EMC club was formed to integrate all branches to work towards developing products and providing solution for real world problems.
- g) **Mech-an-Idea Club:** This is formed by students of Mechanical Engineering to promote DIY.
- h) **Codigo Club:** This is formed by students of Computer Science and Engineering to motivate the coding culture.
- i) **News Letter team:** The institute has published the 1st issue of newsletter entitled as "**From the Hilltop**" with the support from P Sahrudayi Caroline and Vedant Mate along with Faculty in-charge Dr. B Satyasekhar.

10.2 Activities conducted:

- a) **Institute Day and Tree Plantation:** 24th July 2018 was marked as first institute day of IIITDM Kurnool. This day was celebrated by tree plantation on the permanent campus.
- b) **72nd Independence Day** was celebrated and a special felicitation function was organized for the meritorious students of the academic year 2017-2018. Student activity council (SAC) of institute donated books to institute library and cultural functions related to nation were presented.
- c) **Campus Cleaning and Dental Check-up Camp:** Social service group of institute organized clean campus drive and free dental check-up in the institute in association with G. Pulla Reddy Dental College and Hospital on 30th August 2018.
- d) **Ganesh Puja:** Ganesh Chaturthi celebrated by hostel inmates on September 13th, 2018.

- e) **Poster Design** under the **Beti Bachao Beti Padhao Campaign**: A poster Design competition was conducted to commemorate 150 years of father of India's birth anniversary in the various schools in the town of Kurnool. More than 100 students participated in the contest. The top 10 posters were awarded mementos and certificates. The winner was Ms. K. Pravallika of Montessori High School for highlighting the 'Beti Bachao Beti Padao' campaign of Government of India.
- f) **Pongal Festival** in India coincides with other designated state festivals such as 'Bhogali Bihu' in Assam, Lohri in Punjab, 'Bhogi' in Andhra Pradesh and 'Makar Sankranti' all over the country. The Pongal festivities in India are spread over four days of worship, starting from January 14 till January 17. The first day is called 'Bhogi' and the day is spent burning or discarding old and useless items of clothing as well as household items. The same was celebrated in Indian Institute of Information Technology Design & Manufacturing, Kurnool for brining a new dawn of life into the institute.
- g) **The 69th Republic Day** of India was celebrated by the Indian Institute of Information Technology Design & Manufacturing, Kurnool, in high spirits. The national flag was hoisted at 8 am in the institute by one of the faculty members, and gave a moving address to all those present. He urged students to be committed and disciplined and always bear in mind our efforts will contribute to the nation's progress. The celebrations for the day continued at the Indian Institute of Information Technology Design & Manufacturing, Kurnool with a tree plantation drive by staff and students.
- h) **"Hum fit, to India fit" The first edition of the Indian Institute of Information Technology Design & Manufacturing, Kurnool "Republic Run"** washeld on January 27 2019. A 5000 meters short distance running race, the first step towards healthy and balanced lifestyle. The starting point of the race was Vinayak Ghat and the end point was G. Pulla Reddy Engineering College.
- i) On 8th March 2019, Indian Institute of Information Technology Design & Manufacturing, Kurnool celebrated **International Women's Day** holding an event at the institute Seminar Hall. The event was organized by cultural team with great enthusiasm and effort.
- j) The Social Service Group (SSG) of "Indian Institute of Information Technology Design & Manufacturing, Kurnool" organized a **blood donation camp** in association with Indian Red Cross Society on 19th of March, 2019 (Tuesday). The healthy participation by students, staff, and faculty in the event is highly appreciated. During the camp, the total 53 donors reported for blood donation and 53 units of blood were collected.
- k) On 31st May, 2019, Indian Institute of Information Technology Design & Manufacturing, Kurnool organized a session to make aware the faculty members and other non-teaching staffs about harmful effects of tobacco use along with a Pledge ceremony led by honourable director Prof DVLN

Somayajulu. He highlighted the current year theme of **World No Tobacco Day 2019** “Tobacco and Lung Health” to increase the awareness on the negative impact that tobacco on people’s lung health, from cancer to chronic respiratory disease. During his speech, he discouraged the use of tobacco in any form inside the institute premises.

l) **Fifth International Yoga Day** was celebrated with lots of enthusiasm on June 21, 2019 at Indian Institute of Information Technology Design & Manufacturing, Kurnool. Besides group yoga there were several other programs including health talk and meditation.

m) **“Diversity makes the world more beautiful...!”** Going traditional... Students of IIITDM Kurnool celebrated **Ethnic Day** in full pomp and splendour. In the era of modernization and westernization when most of the people are moving away from their own culture and history, events like Ethnic day enable the young generation to revive love and respect for their own culture and history.



BetiBachaoBetiPadao’
campaign of Govt. of India



Institute Day Tree
Plantation



Republic Day



THE SSG GROUP



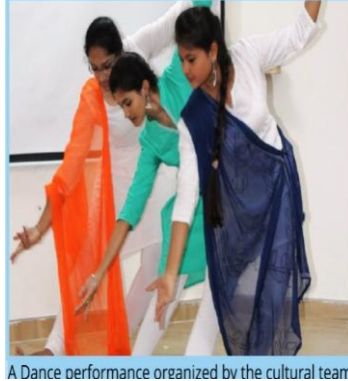
THE SSG LOGO



Unveling of the EMC club logo by
Dr.Suresh Varadarajan



Freshie Night 2k18



A Dance performance organized by the cultural team



11 FINANCE & ACCOUNTS



प्रधान निदेशक लेखापरीक्षा (केंद्रीय) चेन्नै का कार्यालय
लेखापरीक्षा भवन, 361, अण्णा सालै, तेनामपेट, चेन्नै - 600 018.

OFFICE OF THE PRINCIPAL DIRECTOR OF AUDIT (CENTRAL)

Chennai

"LEKHA PARIKSHA BHAVAN", 361, Anna Salai, Teynampet, Chennai - 600 018.

No. PDA (C)/CE/ I/ 28-43/2018-19/

Dt. 03.12.2018

To

The Secretary to Government of India,
Ministry of Human Resource Development,
Department of Education,
New Delhi - 110001

Sir,

Sub: Separate Audit Report on the accounts of Indian Institute of Information Technology, Design and Manufacturing, Kurnool for the year 2017-18- Reg.

I forward herewith the Separate Audit Report on the accounts of Indian Institute of Information Technology, Design and Manufacturing, Kurnool for the year 2017-18 along with the statement of accounts. The dates of presentation of the accounts with Separate Audit Report to Parliament may kindly be intimated to this office.

The receipt of this letter with enclosures may kindly be acknowledged.

Yours faithfully

--Sd--

Encl: As above

Deputy Director/ CE

दूरभाष / Phone : 044 - 2431 6400

फैक्स / Fax : 044 - 2433 8924

Endt. No. PDA (C)/CE/ I/ 28-43/2018-19/ 112

Dt.03.12.2018

Copy together with a copy of the Separate Audit Report forwarded to the **Director, Indian Institute of Information Technology, Design and Manufacturing, Kurnool**. He is requested to furnish three copies of the English version of the **Separate Audit Report** and three copies of the **Annual Report** along with dates of presentation of the Report for the year 2017-18 to Parliament.



Deputy Director/ CE

Separate Audit Report of the Comptroller & Auditor General of India on the Accounts of the Indian Institute of Information Technology, Design and Manufacturing, Kurnool for the year ended 31st March 2018

We have audited the attached Balance Sheet of the Indian Institute of Information Technology, Design and Manufacturing, Kurnool as at 31st March 2018, Income & Expenditure Account and Receipts & Payments Account for the year ended on that date under Section 19 (2) of the Comptroller & Auditor General's (Duties, Powers & Conditions of Service) Act, 1971 read with Section 30 (3) of the Indian Institutes of Information Technology Act, 2014. These financial statements are the responsibility of the Institute's management. Our responsibility is to express an opinion on these financial statements based on our audit.

2 This Separate Audit Report contains the comments of the Comptroller & Auditor General of India (CAG) on the accounting treatment only with regard to classification, conformity with the best accounting practices, accounting standards and disclosure norms, etc., Audit observations on financial transactions with regard to compliance with the Law, Rules & Regulations (Propriety and Regularity) and efficiency-cum-performance aspects, etc., if any are reported through Inspection Report/CAG's Audit Reports separately.

3 We have conducted our audit in accordance with auditing standards generally accepted in India. These standards require that we plan and perform the audit to obtain reasonable assurance about whether the financial statements are free from material misstatements. An audit includes examining, on a test basis, evidences supporting the amounts and disclosure in the financial statements. An audit also includes assessing the accounting principles used and significant estimates made by management, as well as evaluating the overall presentation of financial statements. We believe that our audit provides a reasonable basis for our opinion.

4 Based on our audit, we report that:

i We have obtained all the information and explanations, which to the best of our knowledge and belief were necessary for the purpose of our audit.

ii The Balance Sheet, Income & Expenditure Account and Receipts & Payments Account dealt with by this report have been drawn up in the format approved by Government of India, Ministry of Human Resource and Development.

iii In our opinion, proper books of accounts and other relevant records have been maintained by the Indian Institute of Information Technology, Design and Manufacturing,

Kurnool as required in the rules and regulations of the Institute in so far as it appears from our examination of such books.

iv We further report that:

A General

1. Intangible Assets (Schedule 4) - ₹ 4.93 lakh

The Institute follows the rates of Depreciation as prescribed in the revised formats of Accounts of Central Educational Institutions of MHRD. However, it is noticed that the Institute has adopted 100 percent depreciation for E-journals whereas the rate prescribed by MHRD for E-journals is 40 percent.

2. Effect of revision in accounts

Accounts of the Institute were revised on the basis of Audit comments. As a result of revision, the Loans, Advances and Deposits was reduced by ₹ 2.36 crore and Capital Work-In-Progress was increased by ₹ 2.36 crore. As such, there is no effect on the Balance Sheet and the Surplus arrived at in the Income and Expenditure Account.

B. Grants in aid

The Institute has not received any grants for the year 2017-18. Out of ₹ 11.48 crore being the unspent grant brought forward from the previous year, the Institute could utilize ₹ 2.23 crore leaving a balance of ₹ 9.25 crore as unutilized grant as on 31st March 2018.

v. Subject to our observations in the preceding paragraphs, we report that the Balance Sheet, Income & Expenditure Account and Receipt & Payment Account dealt with by this report are in agreement with the books of accounts.

vi. In our opinion and to the best of our information and according to the explanations given to us, the said financial statements read together with the Accounting Policies and Notes on Accounts, and subject to the significant matters stated above and other matters mentioned in Annexure to this Audit Report give a true and fair view in conformity with accounting principles generally accepted in India.

a. In so far as it relates to the Balance Sheet, of the state of affairs of Indian Institute of Information Technology, Design and Manufacturing, Kurnool as at 31st March 2018 ; and

b. In so far as it relates to Income & Expenditure Account of the surplus for the year ended on that date.

For and on behalf of the C&AG of India

**Place: Chennai
Date: 03.12.2018**


Principal Director of Audit (Central)

Annexure to Separate Audit Report

- 1 Adequacy of Internal Audit System**
Internal audit system is commensurate to the size of the Institute.
- 2 Adequacy of Internal Control System**
Internal control system was adequate
- 3 System of Physical verification of Fixed Assets and Inventory**
IIITDM, Kurnool was shifted to Kurnool campus from July 2018. Physical verification of Fixed Assets and Inventory was not conducted for the year 2017-18.
- 4. Regularity in payment of statutory dues**
The Institute was regular in payment of statutory dues.



Deputy Director/ CE

Financial Statistics for the year 2017-18

Non-plan:

Resource mobilization of the Institute on the **Non-Plan** side during the year 2017-18 was to the tune of **Rs. 1319 lakhs** out of which **Grant-in-aid of Unspent Balance of Rs. 1148 lakhs** was brought down from **F.Y 2016-17** and the rest of **Rs. 171 lakhs** was raised through the internal resources of the Institute comprising of mainly academic fees and interest on savings bank accounts etc.

The **Grant-in-aid of Rs. 1148 lakhs** was the unspent balance of the F.Y. 2016-17. **No Funds** were released by the MHRD, GoI in the year **2017-18**.

The **Non-Plan** expenditure was to the tune **Rs. 247 lakhs** was spent from the **Unspent Balance** of Grant-in-aid for the **F.Y. 2016-17** of the Institute.

Plan:

Nil

**INDIAN INSTITUTE OF INFORMATION TECHNOLOGY,
DESIGN AND MANUFACTURING, KURNOOL**

BALANCE SHEET AS AT 31.03.2018

Amount in Rs.

SOURCES OF FUNDS	Schedule	2017-18	2016-17
CAPITAL FUND	1	8,26,45,423.00	7,53,86,758.00
CORPUS	1A	1,55,36,330.00	64,65,524.00
DESIGNATED/ EARMARKED / ENDOWMENT FUNDS	2	-	-
CURRENT LIABILITIES & PROVISIONS	3	10,38,75,844.00	11,80,80,489.00
TOTAL		20,20,57,597.00	19,99,32,771.00

APPLICATION OF FUNDS	Schedule		
FIXED ASSETS	4		
Tangible Assets		1,05,19,274.00	77,15,528.00
Intangible Assets		4,93,350.00	-
Capital Works-In-Progress		8,75,92,133.00	6,22,62,979.00
INVESTMENTS FROM EARMARKED / ENDOWMENT FUNDS	5		
Long Term			
Short Term			
INVESTMENTS - OTHERS	6		
CURRENT ASSETS	7	4,32,23,150.00	2,89,06,200.00
LOANS, ADVANCES & DEPOSITS	8	6,02,29,690.00	10,10,48,064.00
TOTAL		20,20,57,597.00	19,99,32,771.00

Sd/-
Joint Registrar (Accounts)

Sd/-
Mentor Director

Sd/-
Internal Audit Officer

**INDIAN INSTITUTE OF INFORMATION TECHNOLOGY,
DESIGN AND MANUFACTURING, KURNOOL**

INCOME AND EXPENDITURE ACCOUNT FOR THE PERIOD/YEAR ENDED 31.03.2018

Amount in Rs.

Particulars	Schedule	2017-18	2016-17
INCOME			
Academic Receipts	9	1,63,84,690.00	-
Grants / Subsidies	10	2,23,47,167.00	1,14,54,560.00
Income from investments	11	14,38,444.00	-
Interest earned	12	7,72,291.00	14,74,585.00
Other Income	13	21,058.00	-
Prior Period Income	14	-	-
TOTAL (A)		4,09,63,650.00	1,29,29,145.00
EXPENDITURE			
Staff Payments & Benefits (Establishment expenses)	15	74,81,404.00	37,91,901.00
Academic Expenses	16	66,45,428.00	32,39,714.00
Administrative and General Expenses	17	72,39,867.00	44,11,945.00
Transportation Expenses	18	-	-
Repairs & Maintenance	19	9,80,468.00	11,000.00
Finance costs	20	-	-
Depreciation	4	22,87,012.00	9,73,117.00
Other Expenses	21	-	-
Prior Period Expenses	22	-	3,71,906.00
TOTAL (B)		2,46,34,179.00	1,27,99,583.00
Balance being excess of Income over Expenditure (A-B)		1,63,29,471.00	1,29,562.00
Transfer to / from Corpus Fund		90,70,806.00	
Less: Assets purchased out of Corpus Fund			
Less: Revenue Expenditure met from Corpus Fund			
Building fund			
Others (specify)			
Balance Being Surplus / (Deficit) Carried to Capital Fund		72,58,665.00	
Significant Accounting Policies	23		
Contingent Liabilities and Notes to Accounts	24		

Sd/-
Joint Registrar (Accounts)

Sd/-
Internal Audit Officer

Sd/-
Mentor Director