

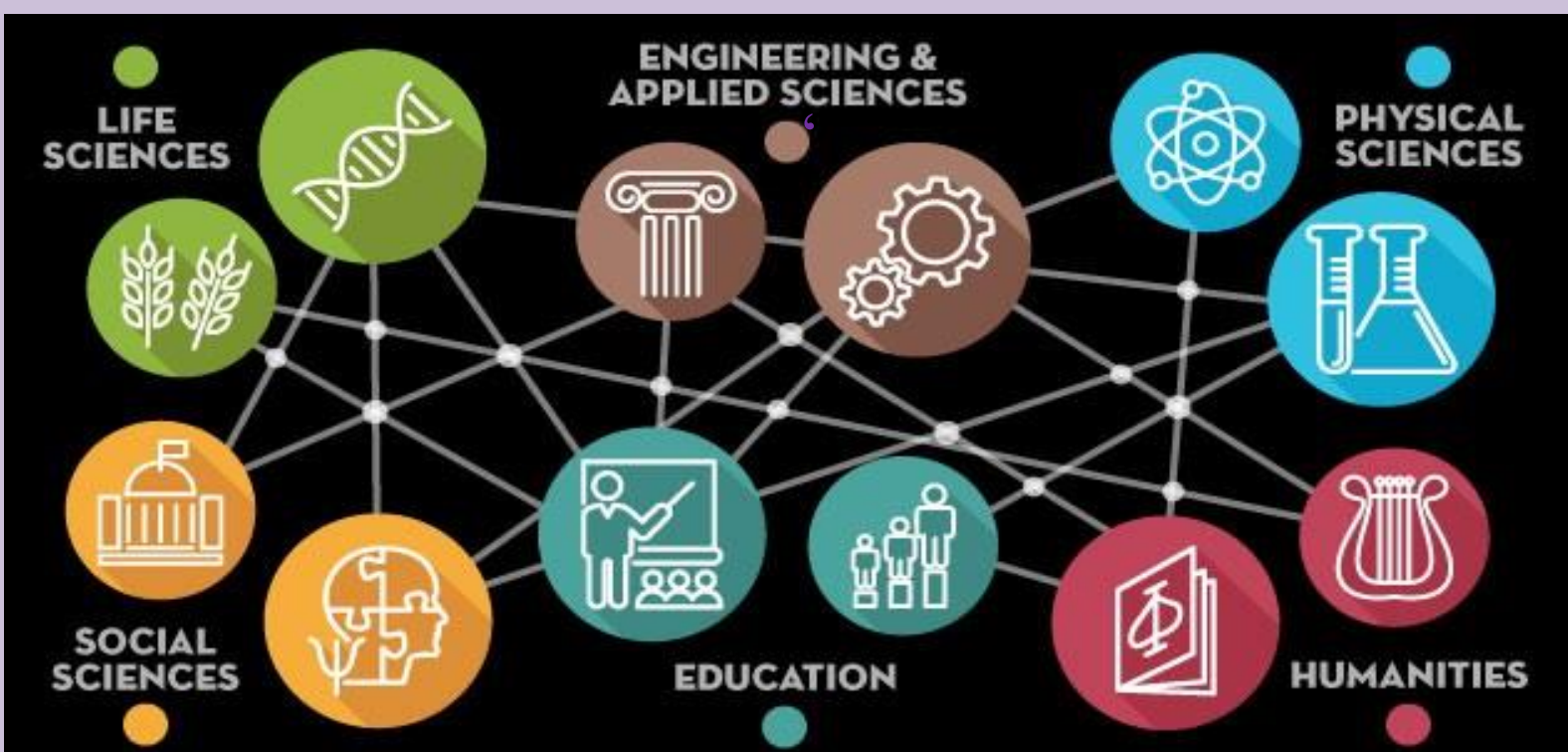
# Indian Institute of Information Technology Design and Manufacturing Kurnool



## Information Brochure

### Ph.D. admissions January 2025 Session

**Centre for Interdisciplinary Research**



## About the Centre for Interdisciplinary Research:

IIITDM Kurnool announces Interdisciplinary (ID) PhD program for enthusiastic aspirants exploring the interface of various science and engineering disciplines. The ID PhD Program involves rigorous course work (12 or 24 credits depending on the academic background of students) followed by research work in an interdisciplinary research topic leading to a doctoral thesis.

## Vacancies:

Full-Time Ph.D.	
Institute Fellowship (HTRA)	5

## Eligibility Criteria for Full-Time Ph.D.:

### (a) Applicants holding Master's degree:

- M. Tech./MSc/M.S. (Research) degree in Engineering/Technology in appropriate branch of study from any CFTI with a minimum CGPA of 7.5/10.0 or 70% aggregate of marks at both UG and PG.
- M. Tech./MSc/M.S. (Research) degree in Engineering/Technology in appropriate branch of study with a minimum CGPA of 6.5/10.0 or 60% aggregate of marks at both UG and PG for UR/OBC/EWS category and at least CGPA of 6.0/10 or 55% aggregate of marks at both UG and PG for candidates under SC/ST/PWD category. (**GATE Qualification Mandatory**).

### (b) Applicants holding Bachelor's degree:

- B. Tech. degree holder of any CFTI with a minimum CGPA of 7.5/10 or 70% aggregate marks.
- B. Tech. degree holder of any CFTI with a **valid GATE score** and with a minimum CGPA of 7.0/10 or 65% aggregate marks for UR/OBC/EWS category and minimum of 60% aggregate marks or 6.5 CGPA for candidates under SC/ST/PWD category.
- B. Tech. degree with **valid GATE score** and at least CGPA of 7.5/10 or 70% aggregate of marks under UR/OBC/EWS category and at least CGPA of 7.0/10 or 65% aggregate for candidates under SC/ST/PWD category.

## Broad research areas:

- Design, Simulation and Development of Near IR Photodetectors
- Modeling and Fabrication of High Energy Density Flexible Super Capacitors
- Quantum Machine Learning - Application to structural optimization
- Privacy-Preserving Cryptography for Block-chain Applications
- Cache template attack on block ciphers
- High Performance Hardware Accelerators on Physical Systems using FPGA
- High Performance Hardware Accelerators on Theoretical Computer Science using FPGA and ASIC Technologies
- High Performance Hardware Accelerators on Steganography using FPGA and ASIC Technologies
- High Performance Network on Chip (NoC) Design using Instruction and Data Level Parallelism
- High Performance Hardware Accelerators on Finite Element Analysis
- Experimental and machine learning assisted design and development of high capacity fast charging EV thermal management systems
- Skyrmion devices for memory and logic applications
- Design of Portable NMR spectrometer for identification Protein interactions

- Design of hardware accelerators for LLMS in healthcare
- AI-Driven Robotic Navigation and Path Planning for Autonomous Exploration in Complex Terrains
- Open Quantum Control Systems.

**Convener/coordinator of Ph.D. admissions and contact details:**

- Academic Section: 08518289-121

**Important Dates:**

Web notification of the PhD Advertisement	04/12/2024
Online application registration process start date	05/12/2024
Last date for the submission of online Application form	22/12/2024, 11:59PM
Notification of shortlisted candidates for Interview/Written Test	24/12/2024
Tentative dates for Interview/Written Test	30/12/2024
Publication of Final Results	01/01/2025
Last date for seat acceptance and fee payment:	04/01/2025
Reporting to the Institute	09/01/2025